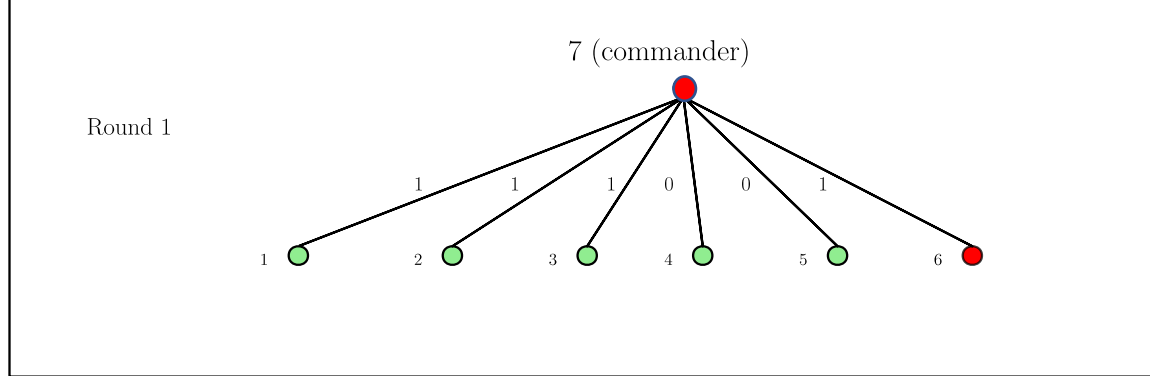
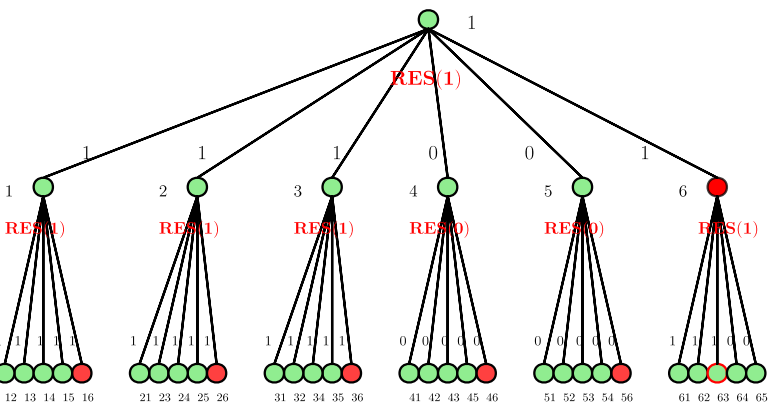


EIG Examples

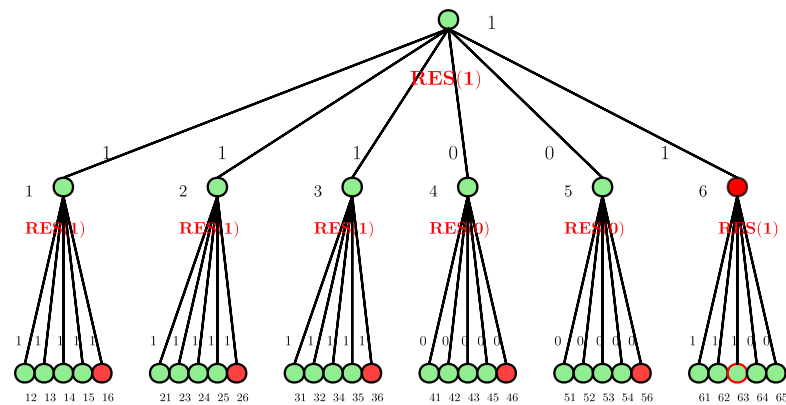
Two scenarios



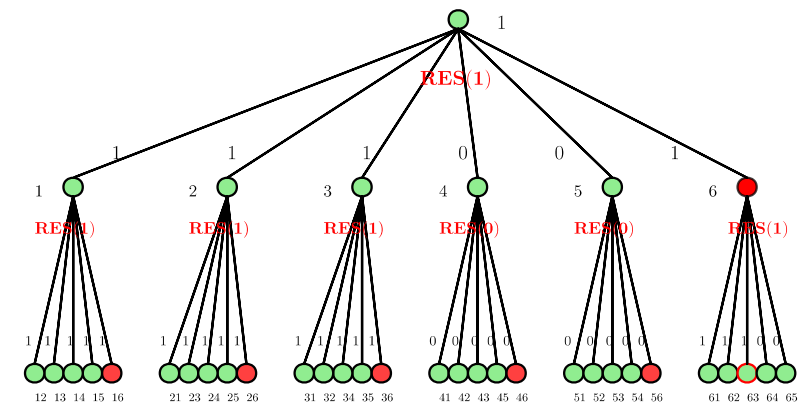
processor 1



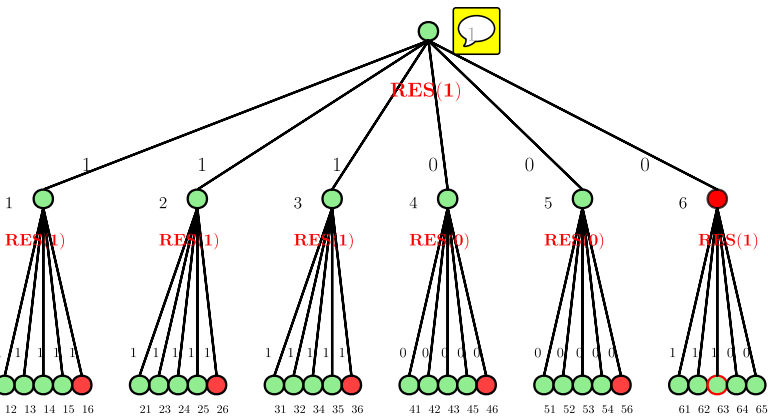
processor 2



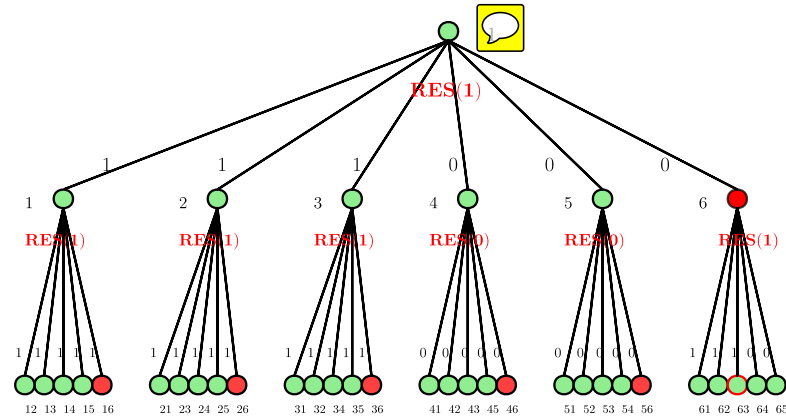
processor 3



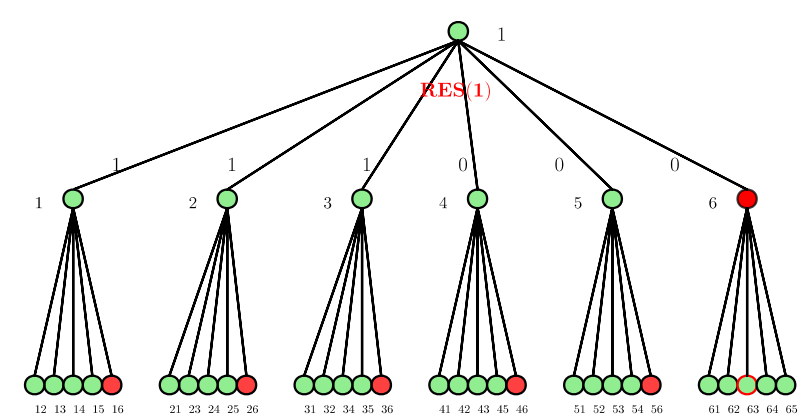
processor 4



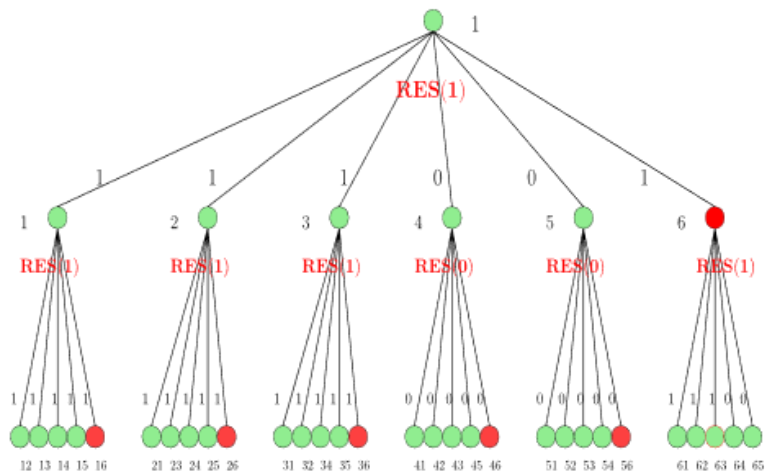
processor 5



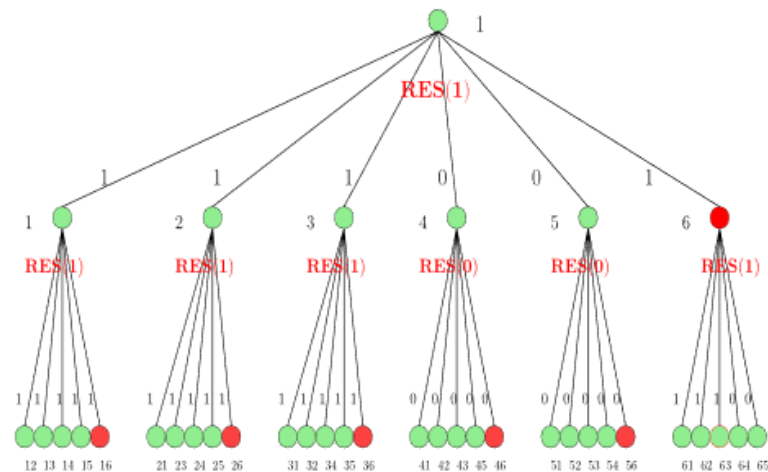
processor 6



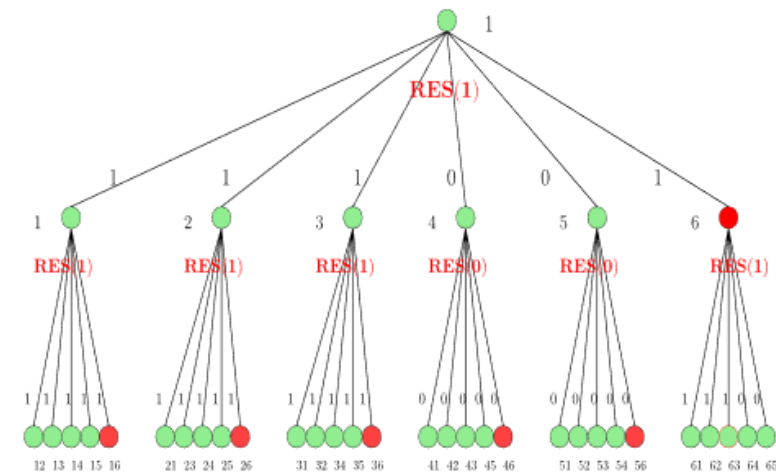
processor 1



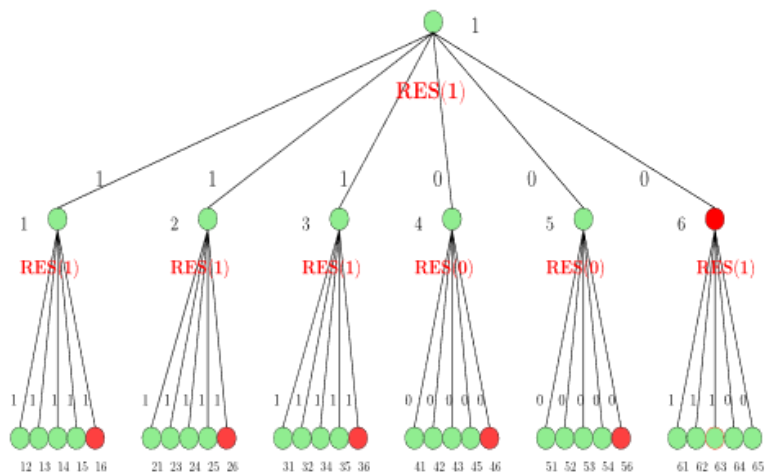
processor 2



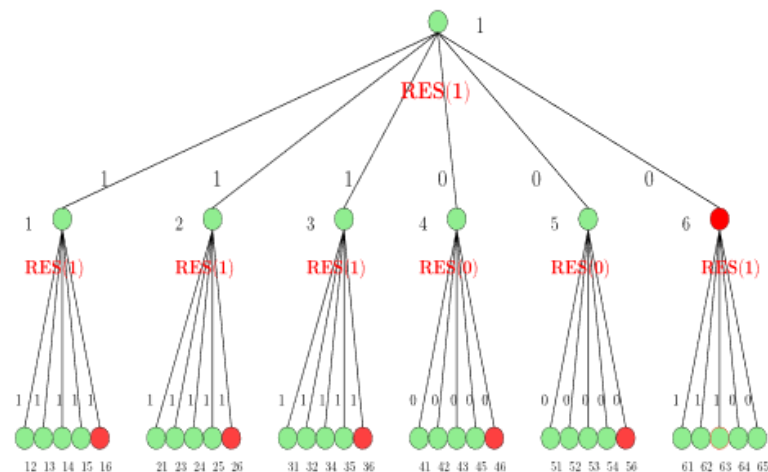
processor 3



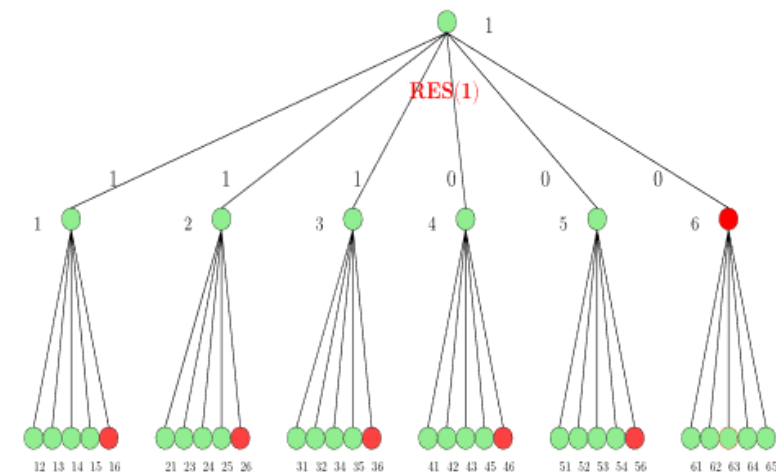
processor 4

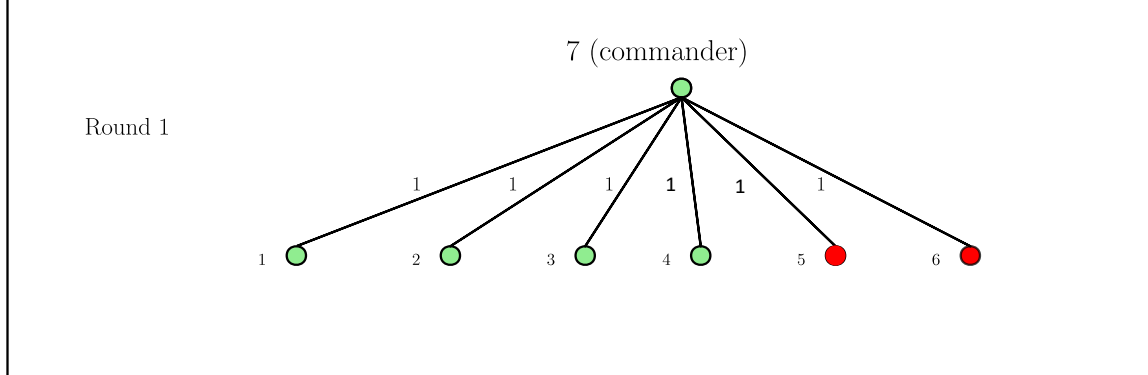


processor 5

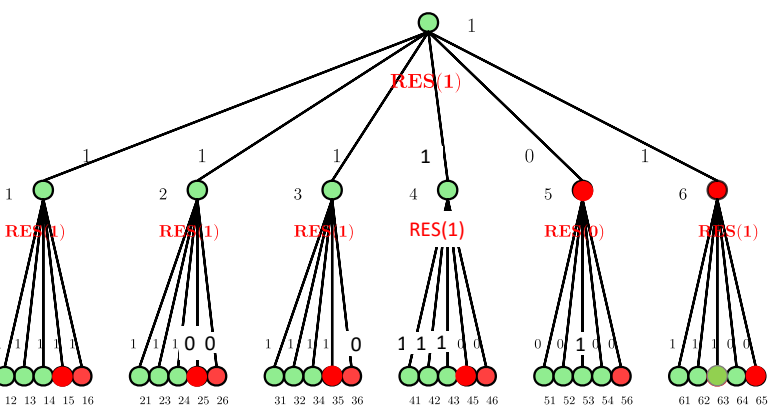


processor 6

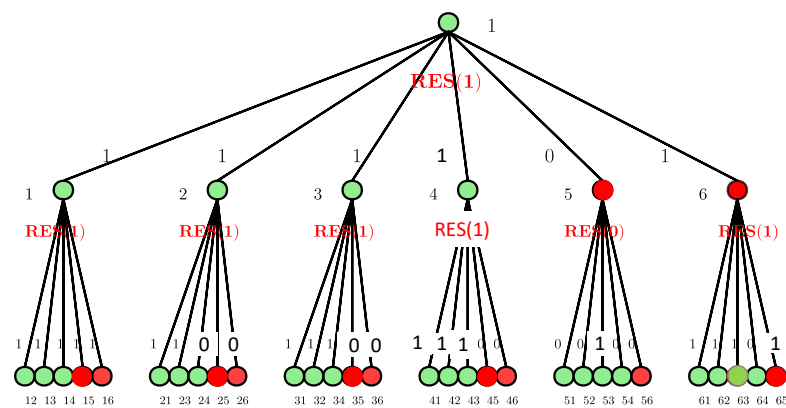




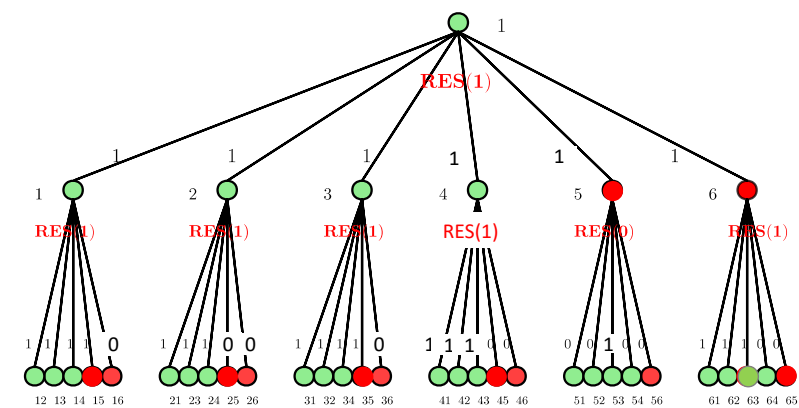
processor 1



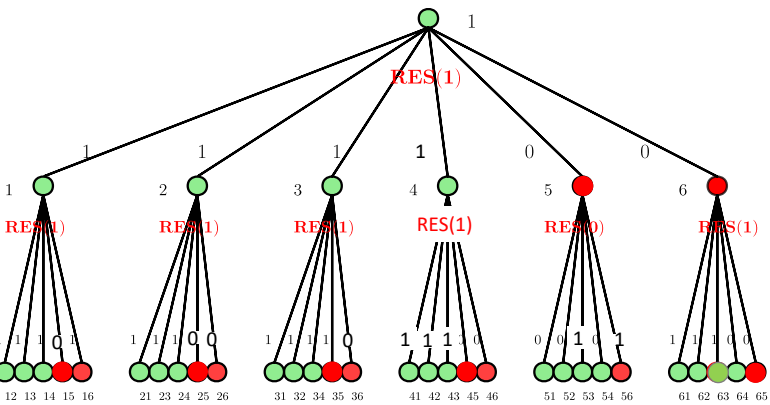
processor 2



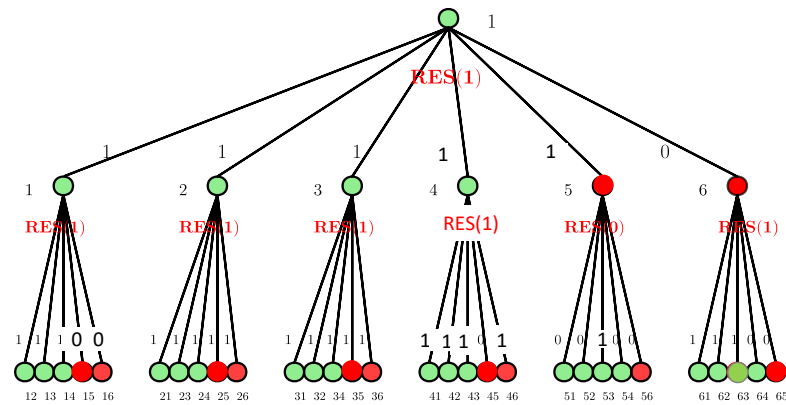
processor 3



processor 4



processor 5



processor 6

